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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**RECEIVED**

**AUG 31 2001**

**Technology Center 2100**

Applicant: Rajeev K. Nalawadi

Serial No.: 09/893,718 ✓

Filed: June 29, 2001 ✓

For: **A COMPUTER SYSTEM HAVING A SINGLE PROCESSOR  
EQUIPPED TO SERVE AS MULTIPLE LOGICAL PROCESSORS  
FOR PRE-BOOT SOFTWARE TO EXECUTE PRE-BOOT TASKS IN  
PARALLEL**

Art Unit: Not Yet Assigned

Examiner: Not Yet Assigned

**PRELIMINARY AMENDMENT**

Assistant Commissioner for Patents  
Washington, DC 20231

**August 2, 2001**

Sir:

Entry of the following preliminary amendments to the above-identified application,  
prior to consideration and examination, is respectfully requested.

**IN THE CLAIMS**

Please **add claims 25-40**, as follows:

- A'
- 1 --25. A system, comprising:
  - 2 a memory comprising first instructions associated with a first startup initialization
  - 3 task and second instructions associated with a second startup initialization task; and
  - 4 a processor to execute the first instructions associated with the first startup
  - 5 initialization task and to initiate execution of the second instructions associated with the
  - 6 second startup initialization task prior to completing execution of the first instructions.

08/06/2001 MYUSUF1 00000084 09893718

01 FC:103 288.00 OP  
02 FC:102 240.00 OP